

Fig. 1

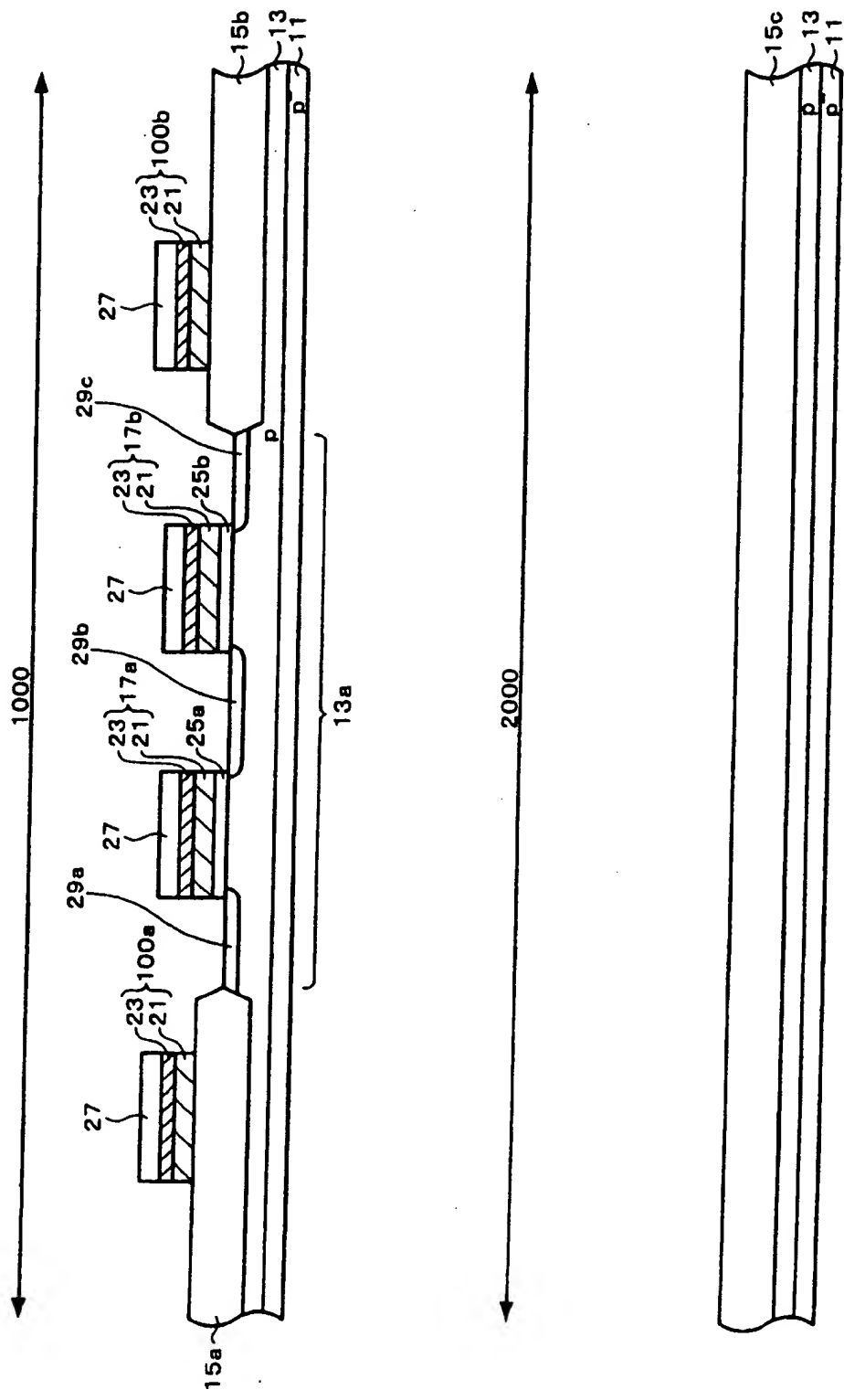


Fig. 2





FIG. 5 is a cross-sectional view of the device of FIG. 1, taken along line 5-5 of FIG. 1, showing the device in a second state of operation. The device is shown in a second state of operation, with the first electrode 11 and the second electrode 11' being in contact with the first conductive layer 31 and the second conductive layer 33, respectively. The first conductive layer 31 and the second conductive layer 33 are shown in a second state of operation, with the first conductive layer 31 being in contact with the first electrode 11 and the second conductive layer 33 being in contact with the second electrode 11'. The first conductive layer 31 and the second conductive layer 33 are shown in a second state of operation, with the first conductive layer 31 being in contact with the first electrode 11 and the second conductive layer 33 being in contact with the second electrode 11'.

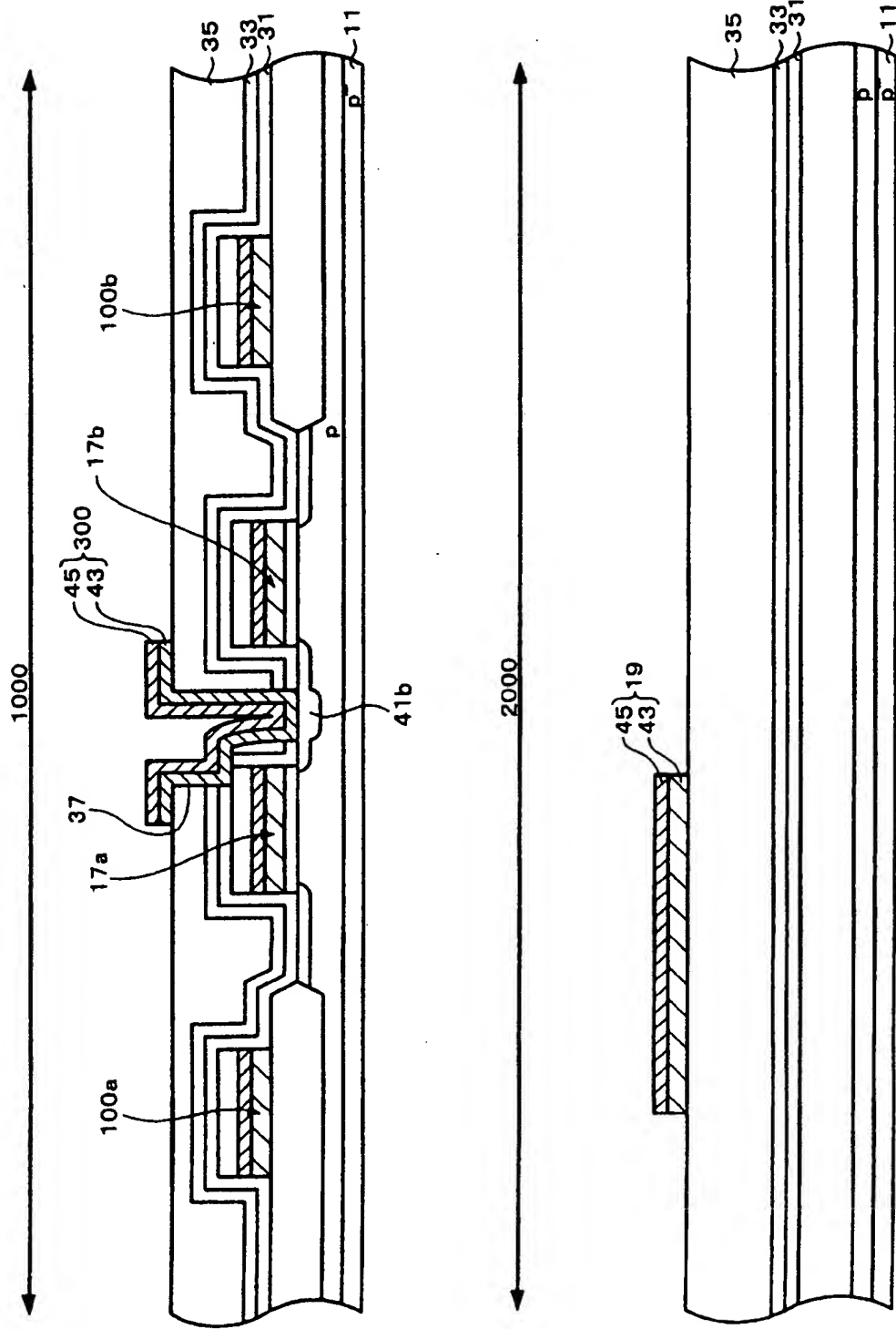


Fig. 5

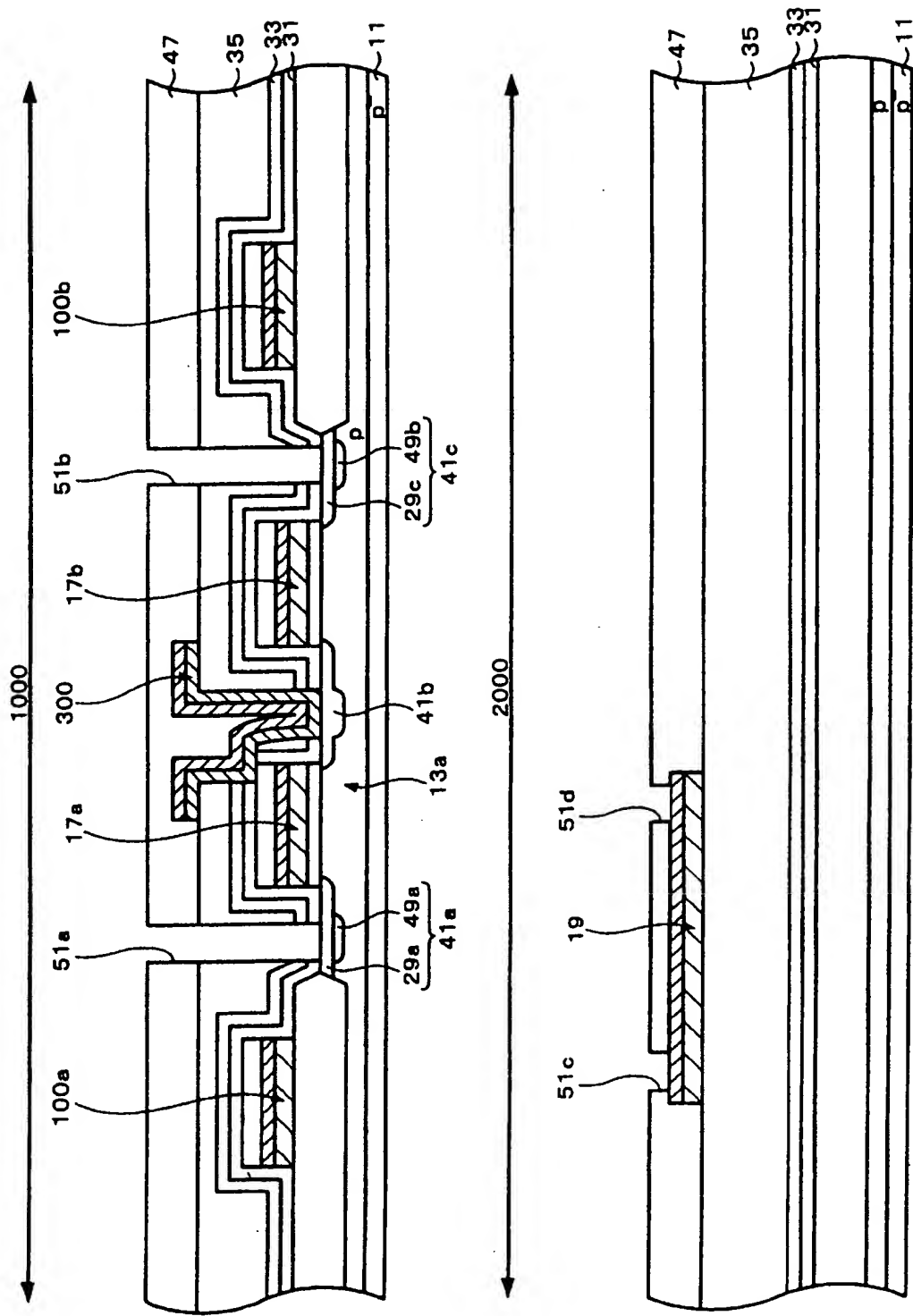


Fig. 6

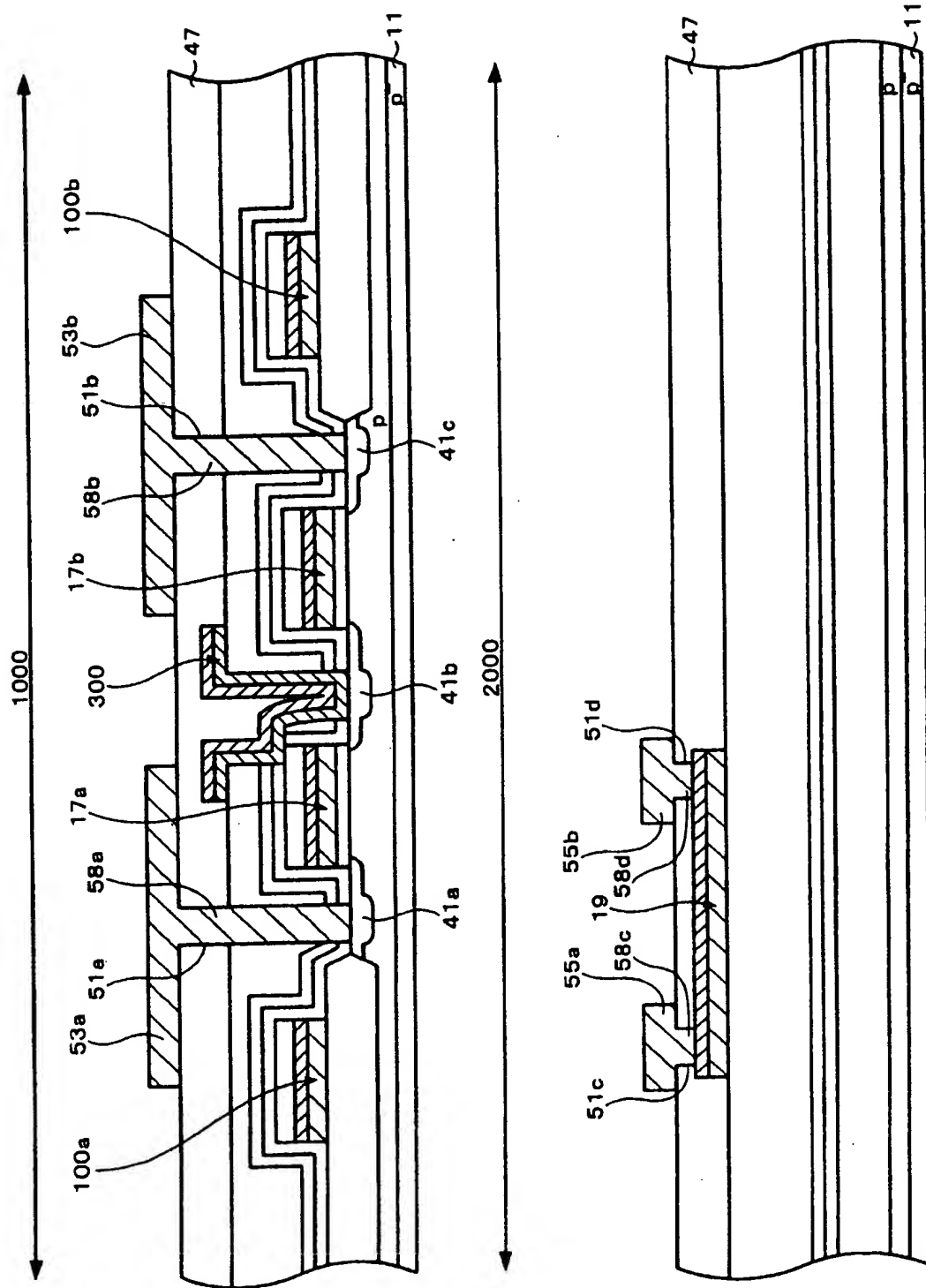


Fig. 7

FIG. 8 is a cross-sectional view of a semiconductor device in accordance with the present invention, showing a top view of the device. The device includes a substrate 11, a gate stack 17a, 17b, and a source/drain region 100a, 100b. The gate stack 17a, 17b is formed on the substrate 11, and the source/drain region 100a, 100b is formed on the gate stack 17a, 17b. The device is shown in a cross-sectional view, with the substrate 11 at the bottom, the gate stack 17a, 17b in the middle, and the source/drain region 100a, 100b at the top. The device is shown in a cross-sectional view, with the substrate 11 at the bottom, the gate stack 17a, 17b in the middle, and the source/drain region 100a, 100b at the top.

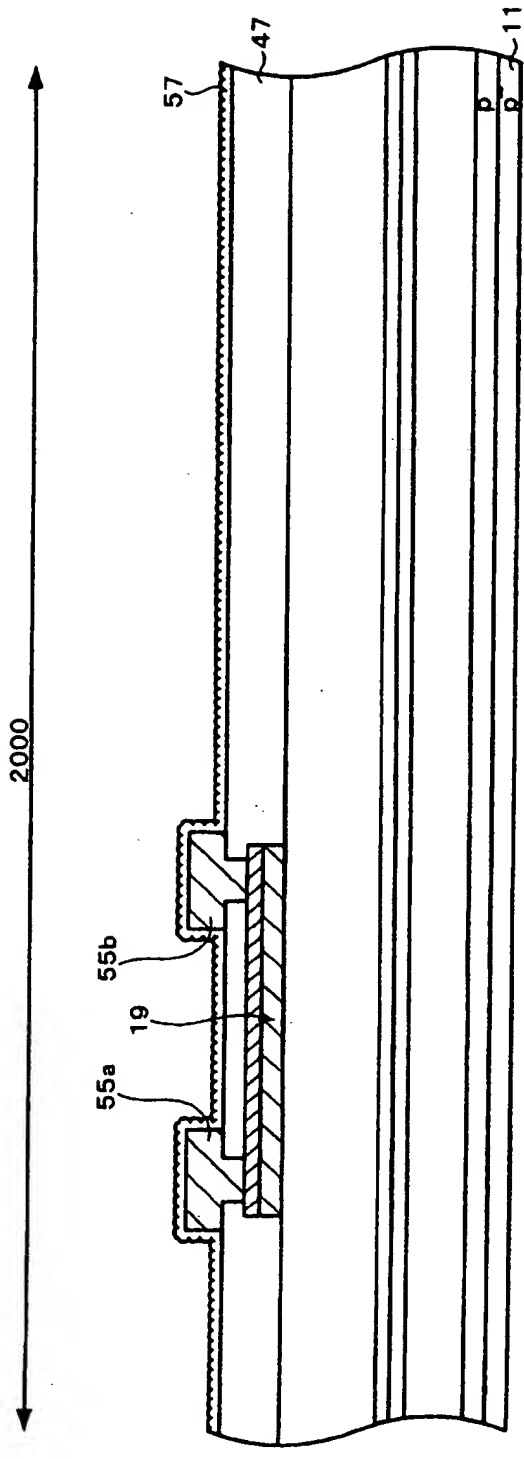
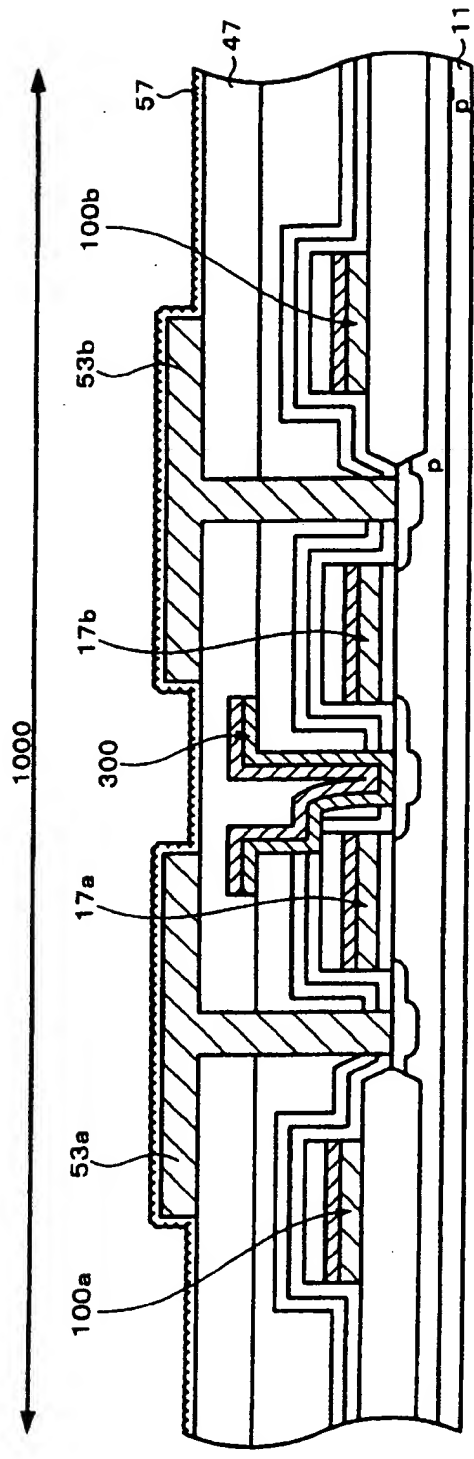


Fig. 8



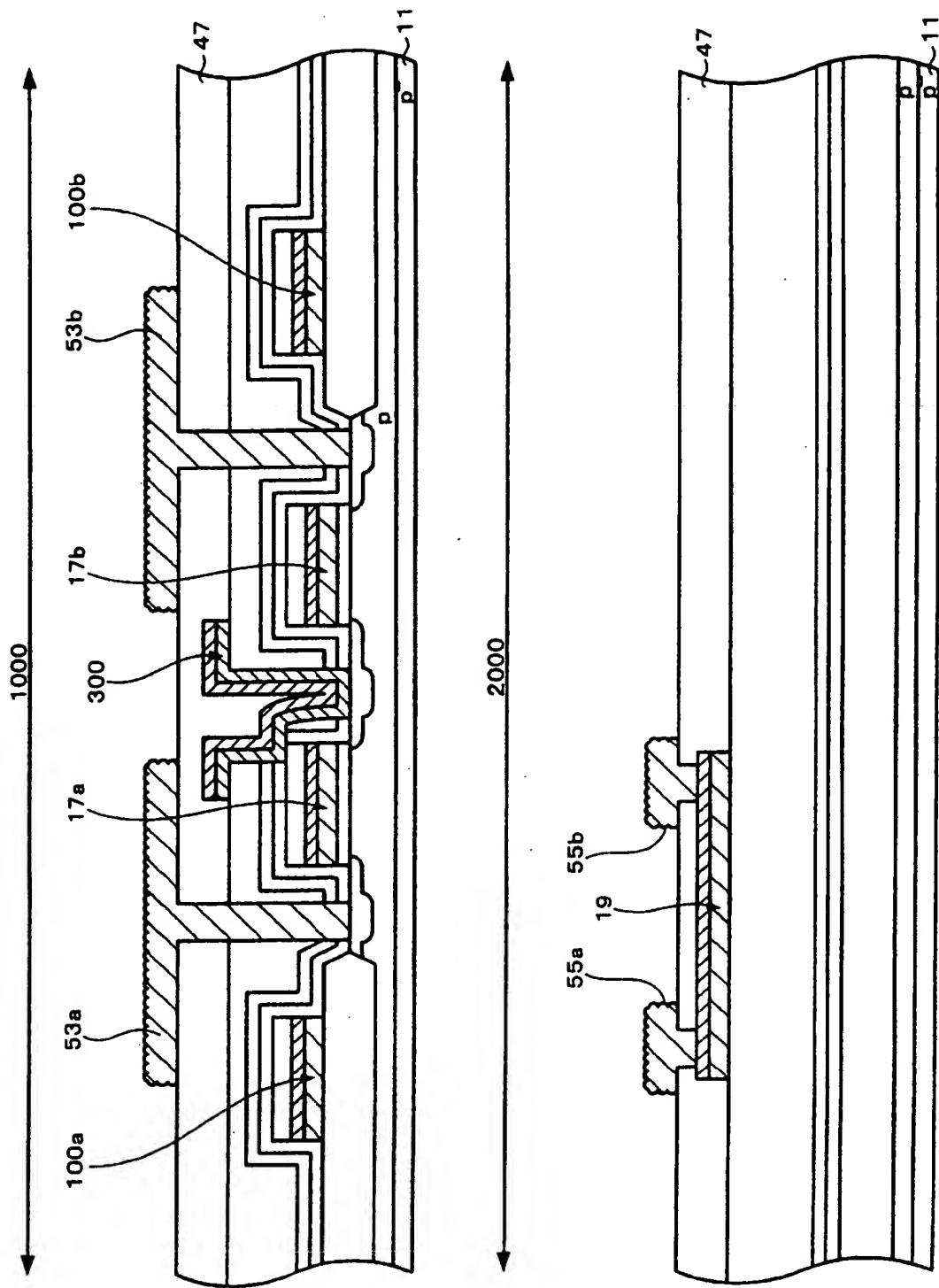


Fig. 9

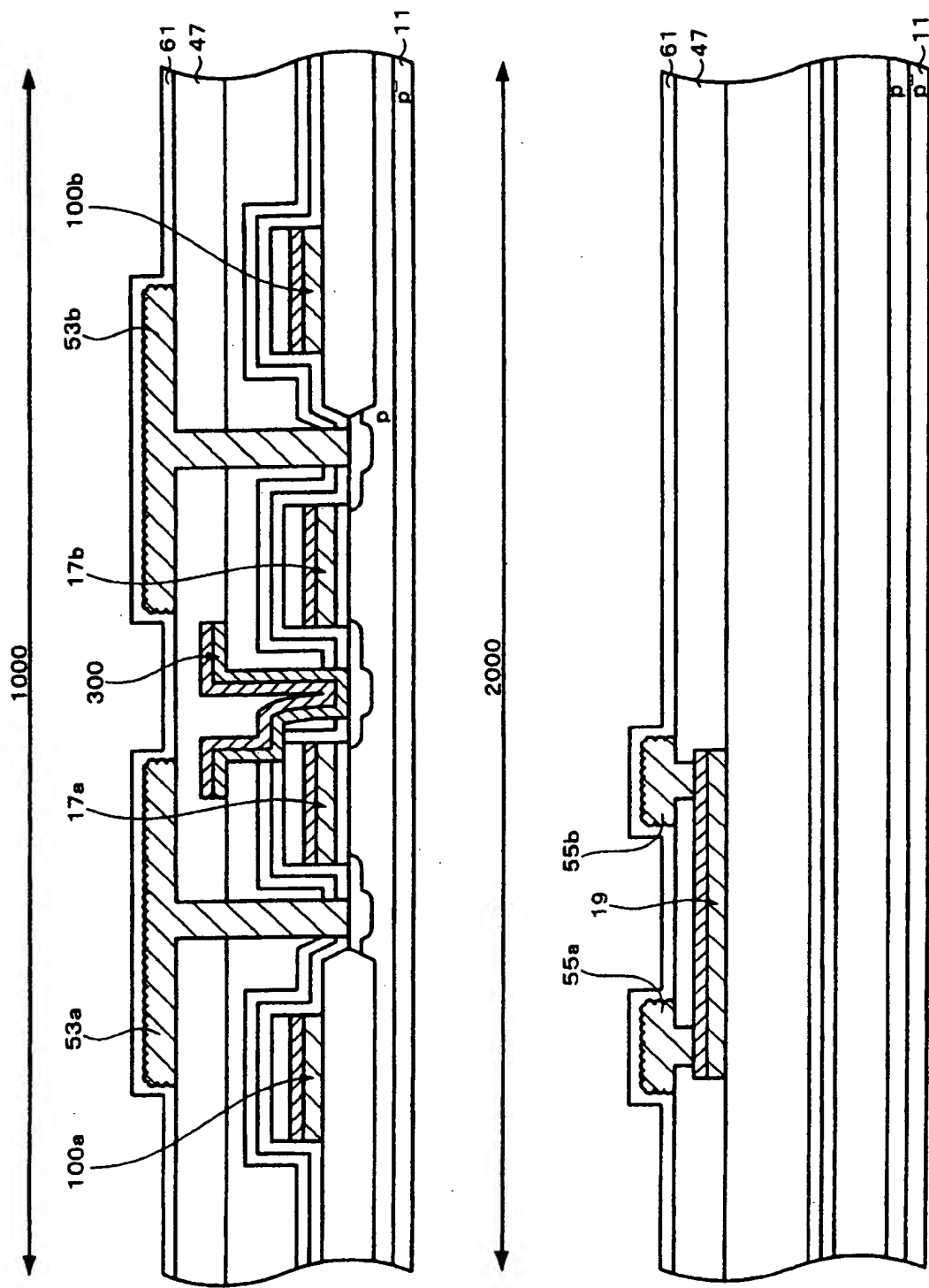


Fig. 10



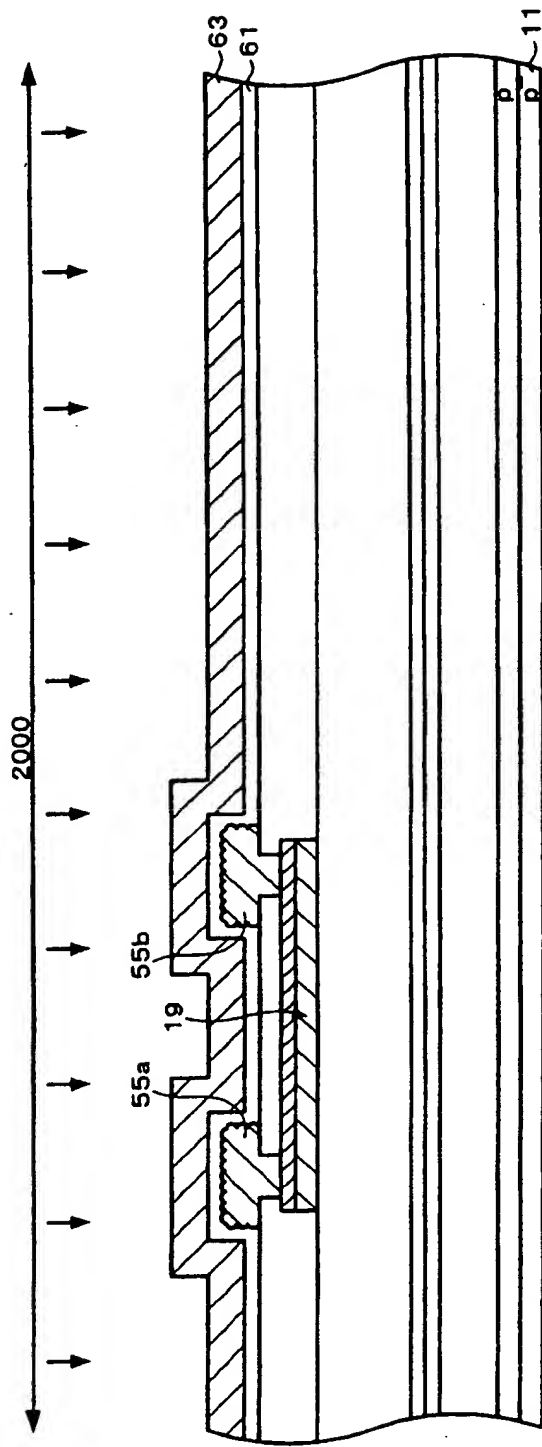
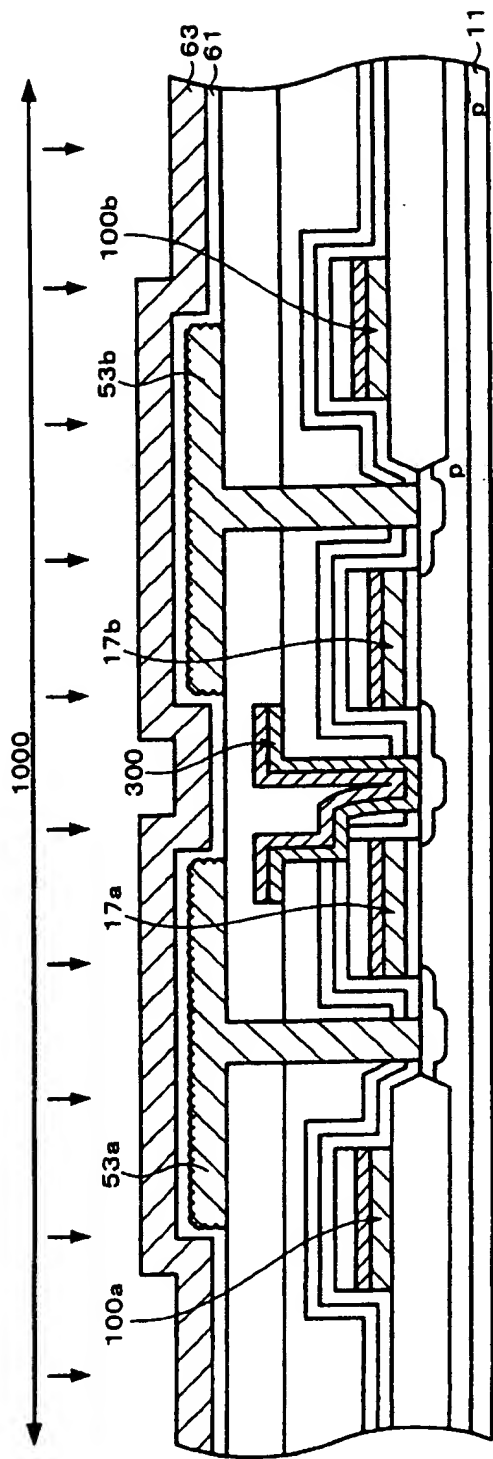


Fig. 12

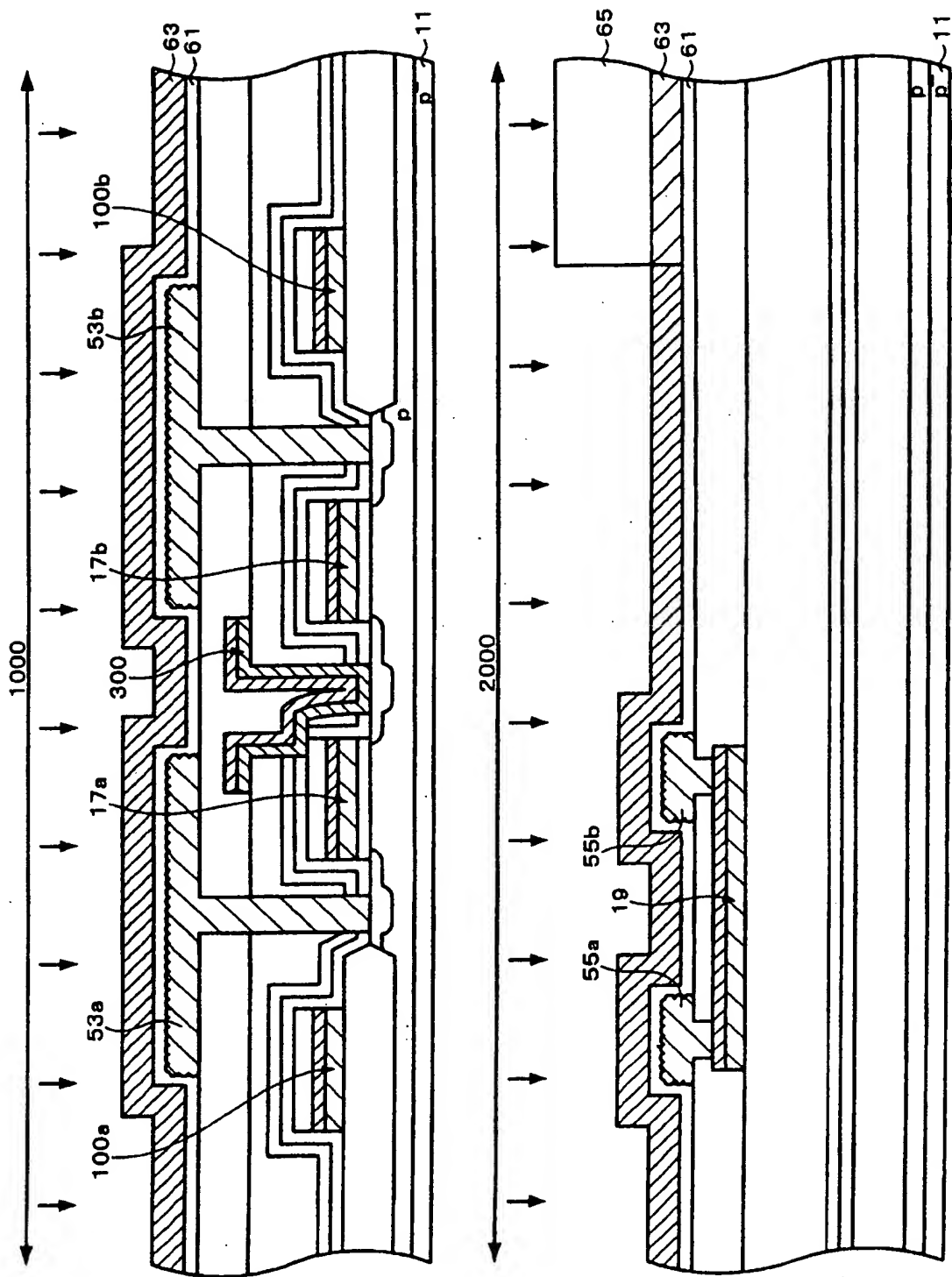
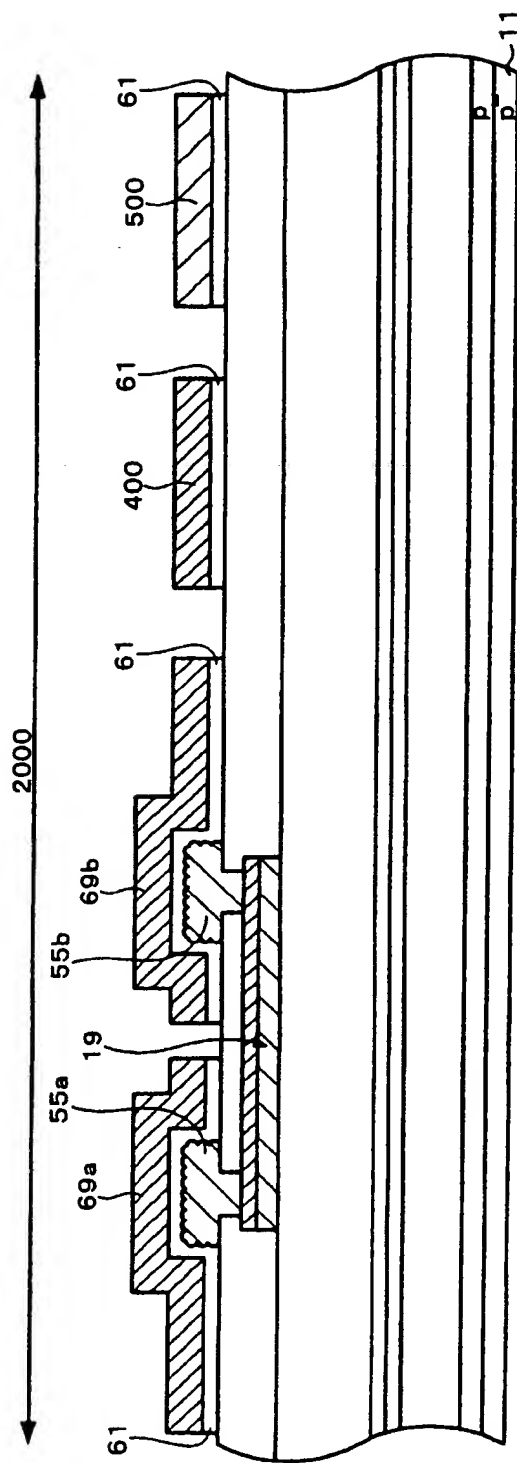
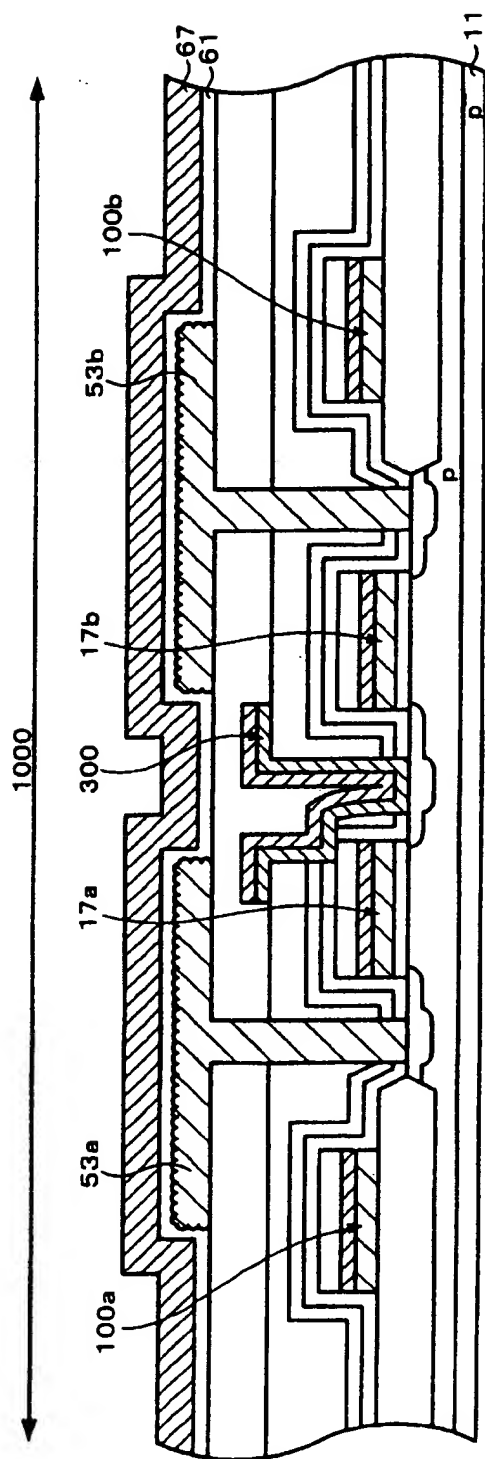
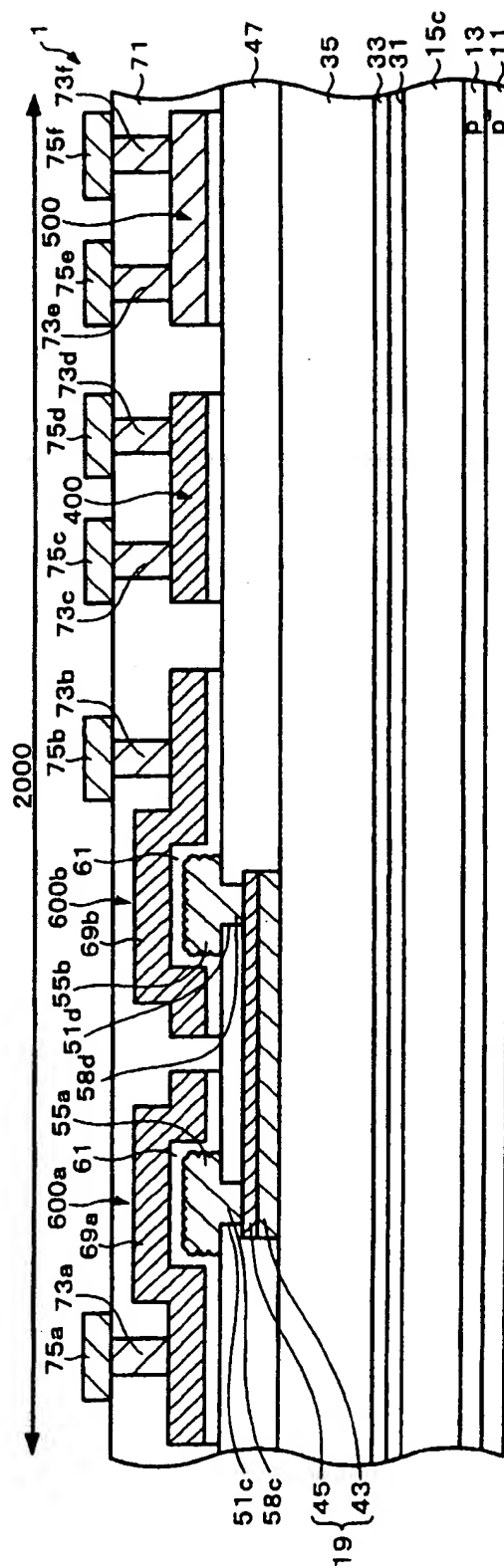
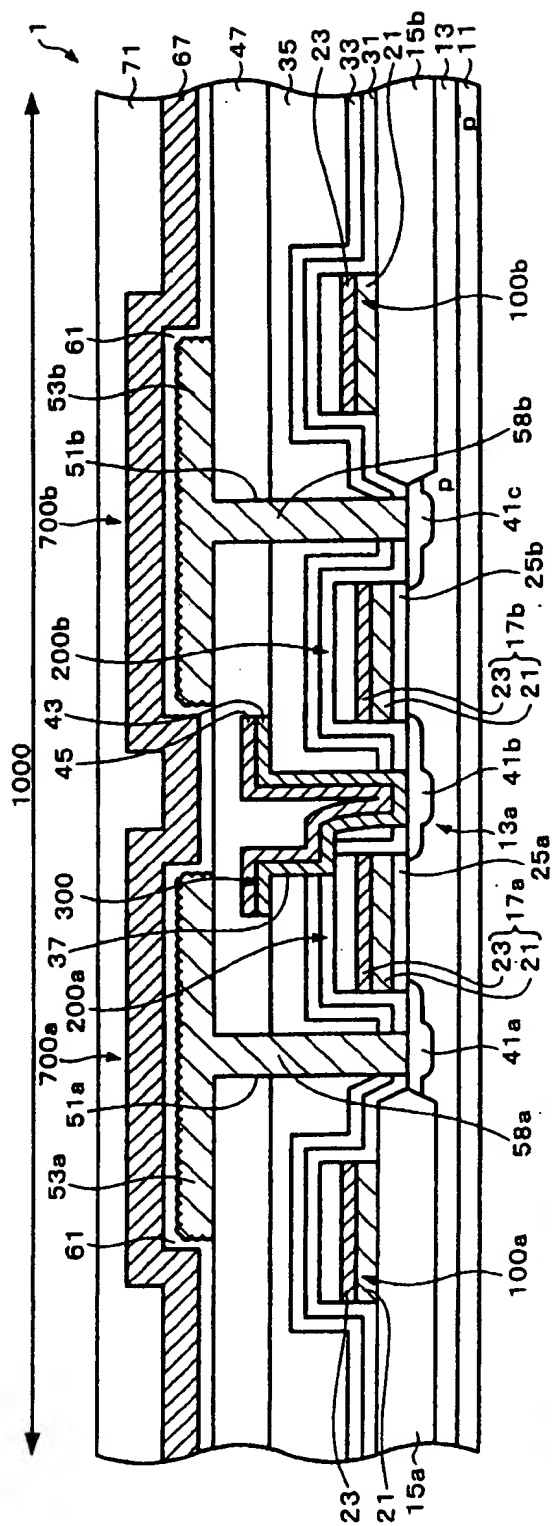


Fig. 13



**Fig. 14**



**Fig. 15**

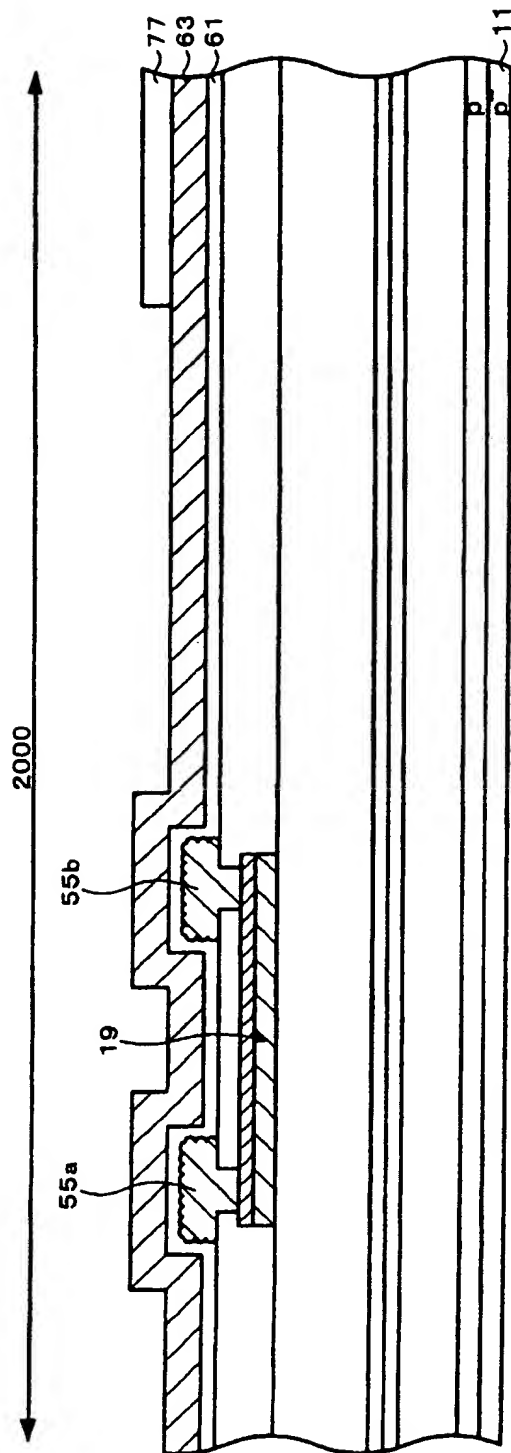
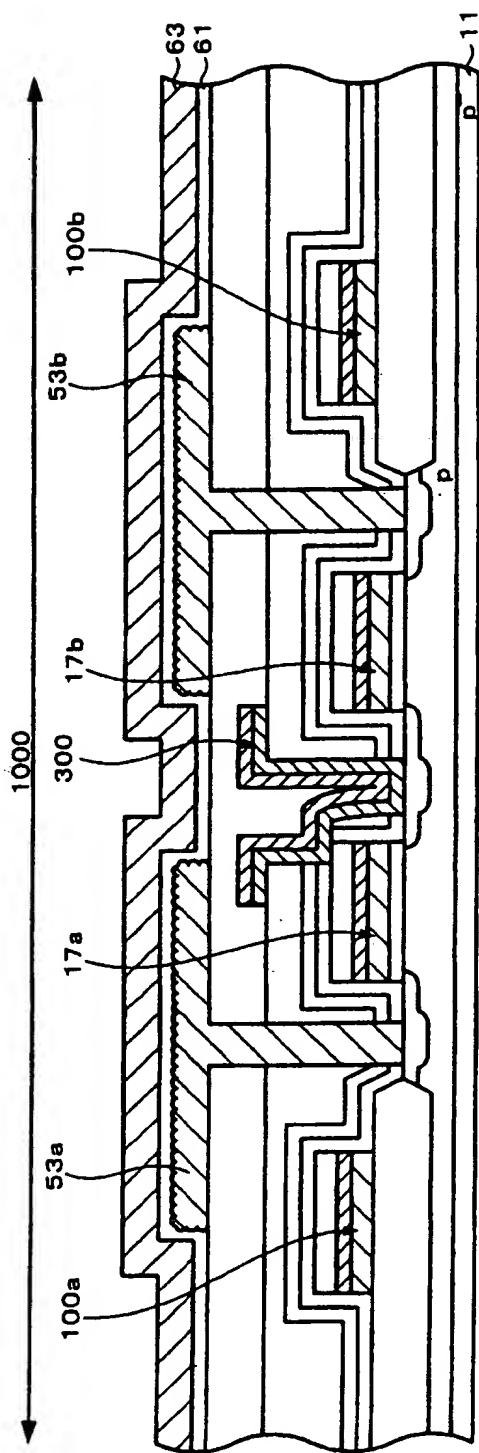


Fig. 16



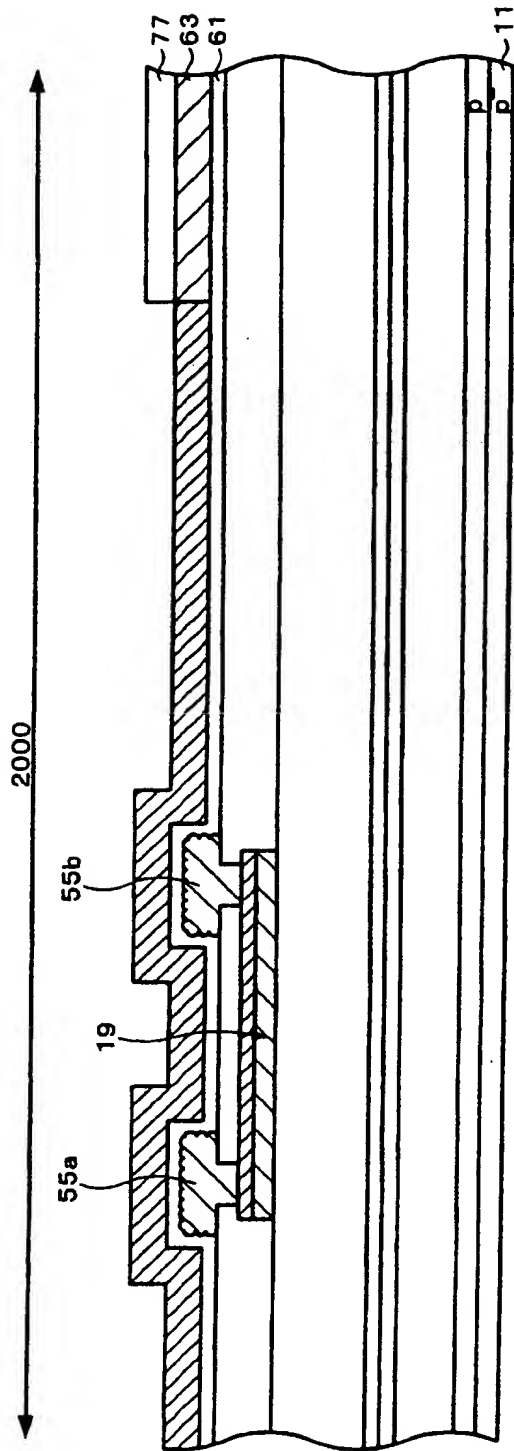
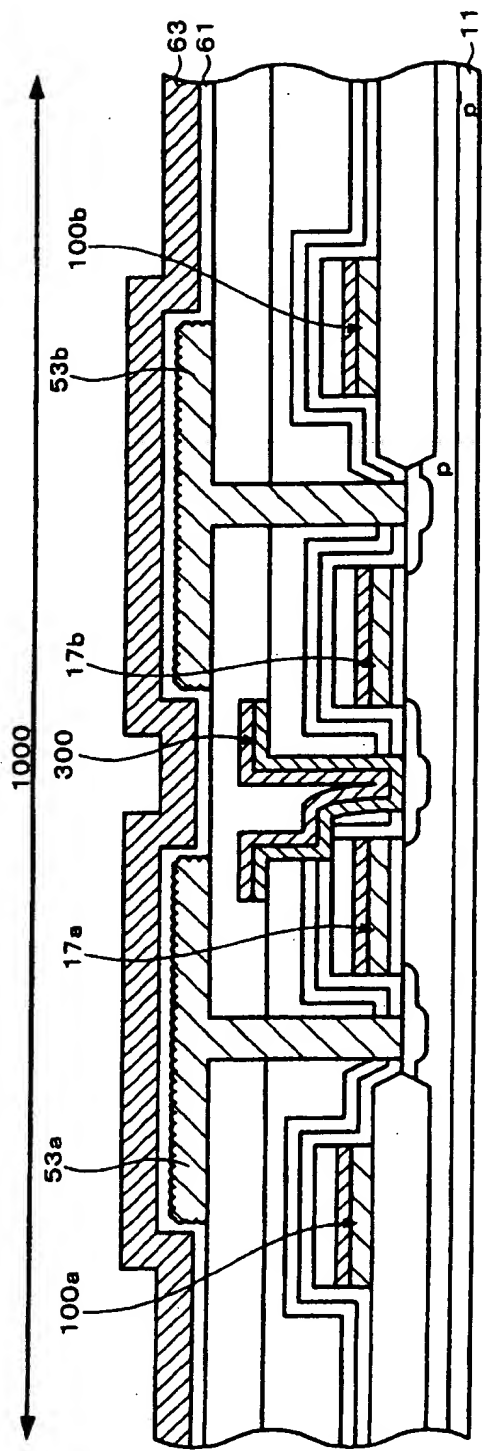
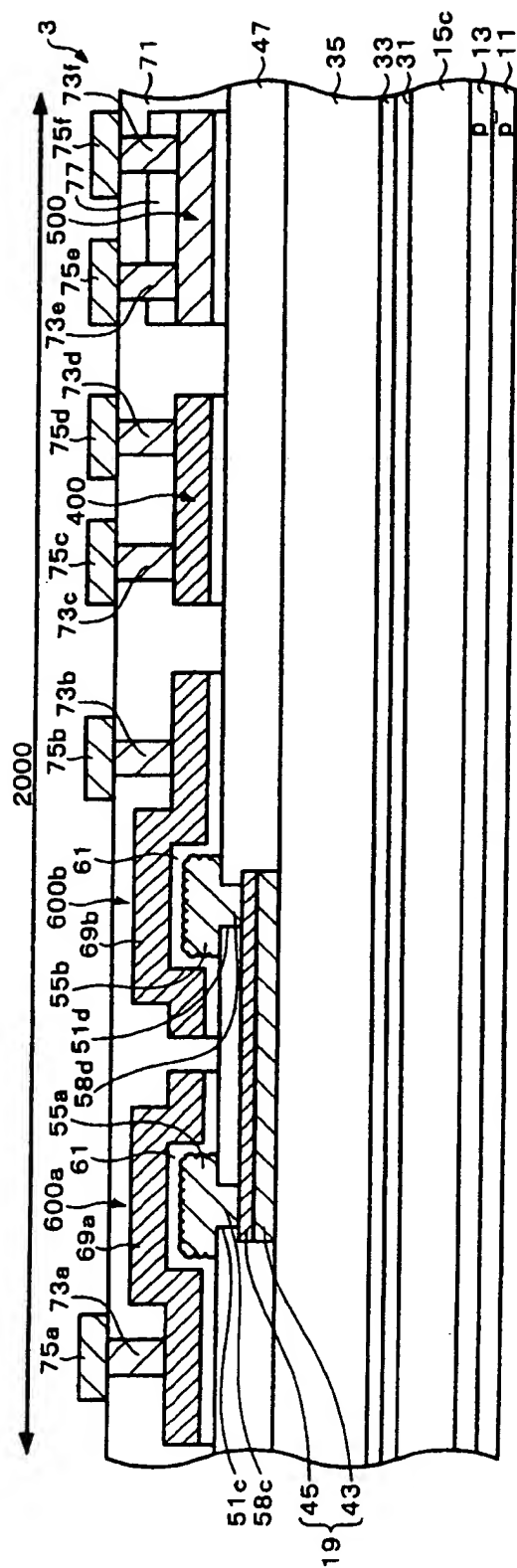
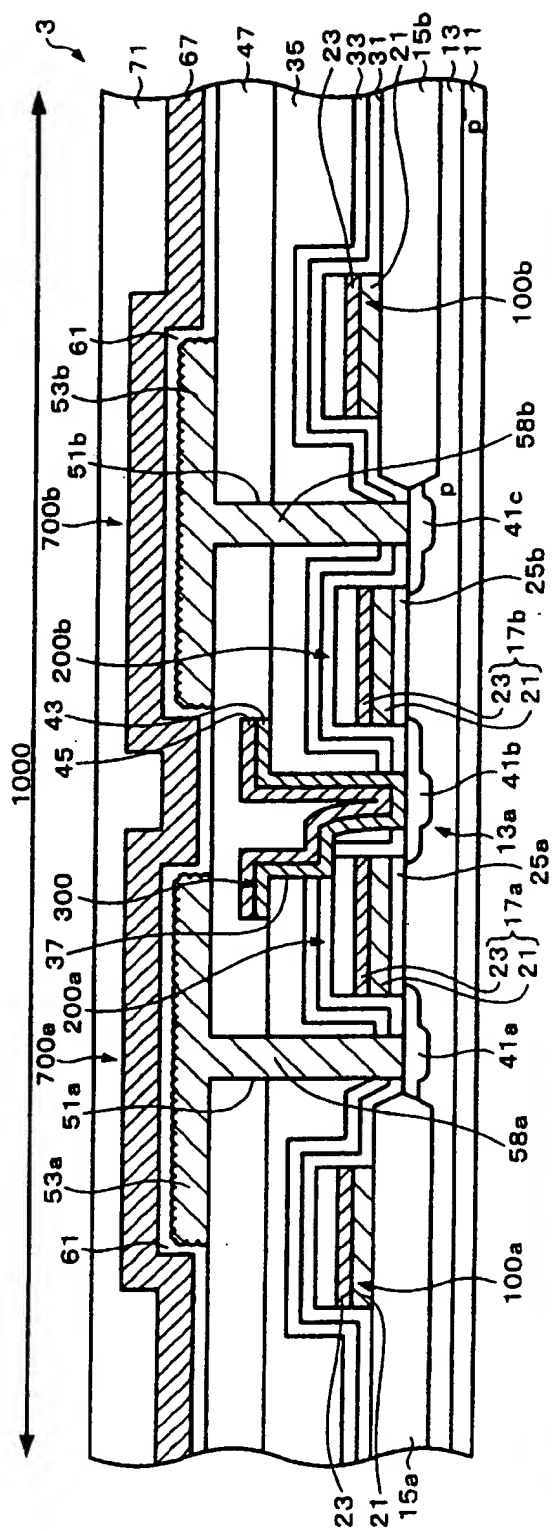


Fig. 17



**Fig. 18**

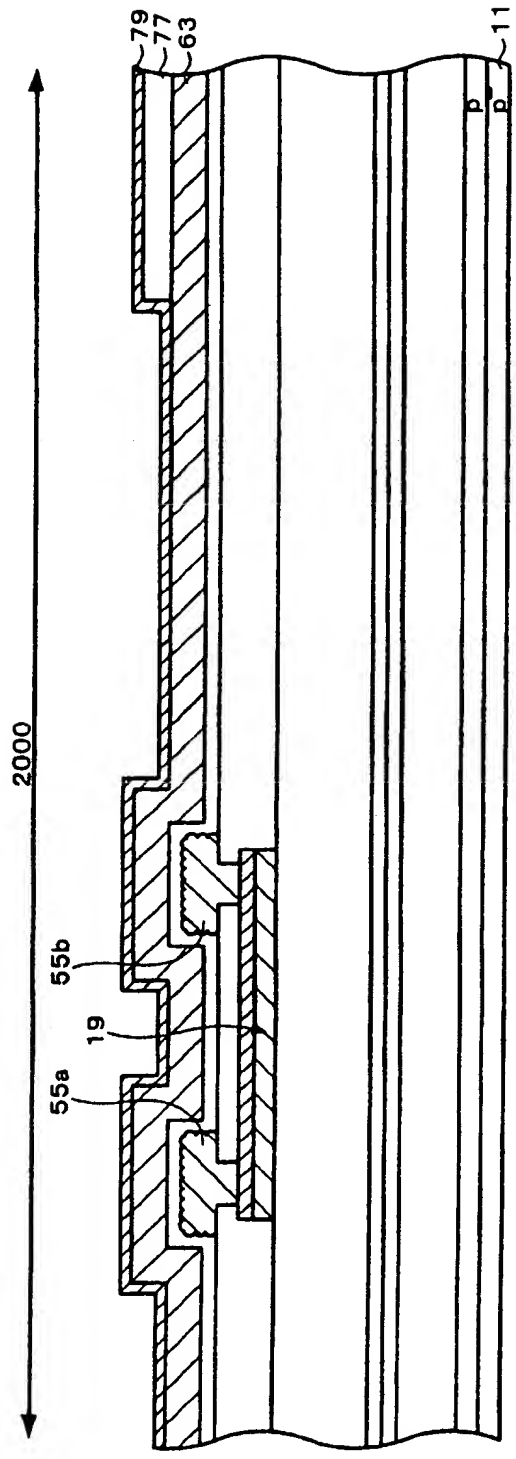
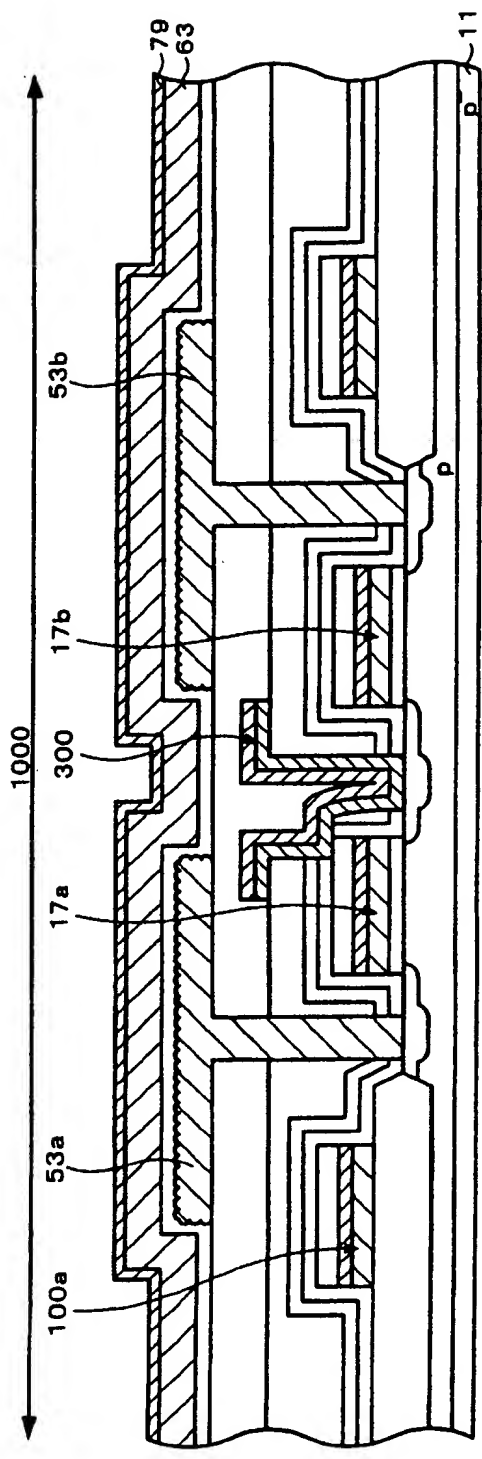


Fig. 19

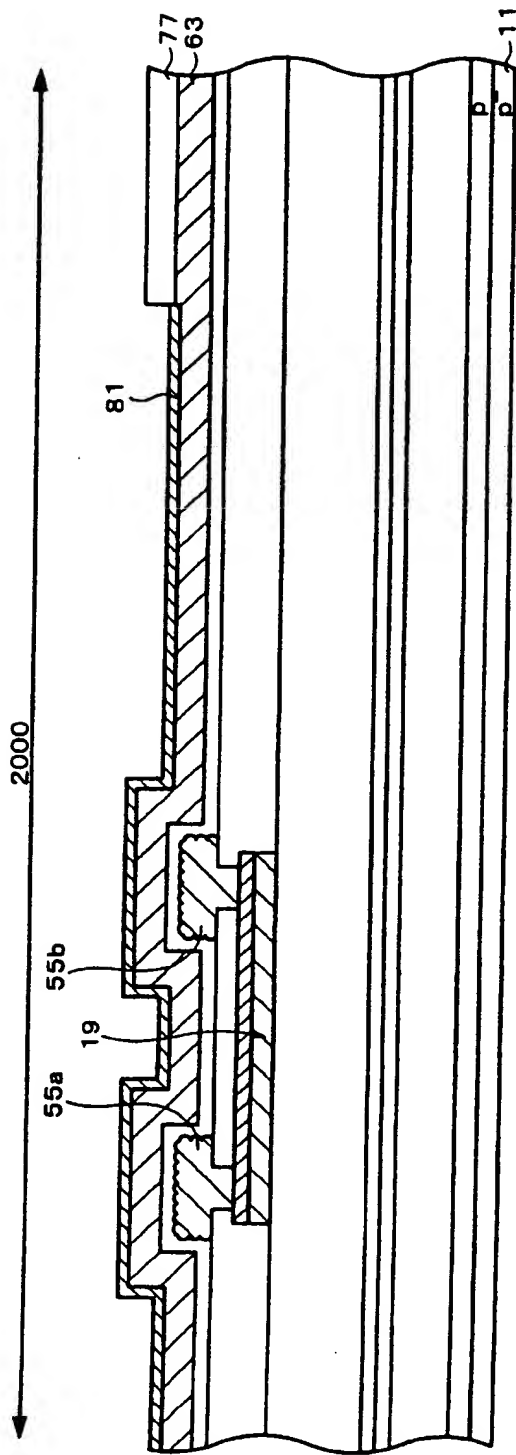
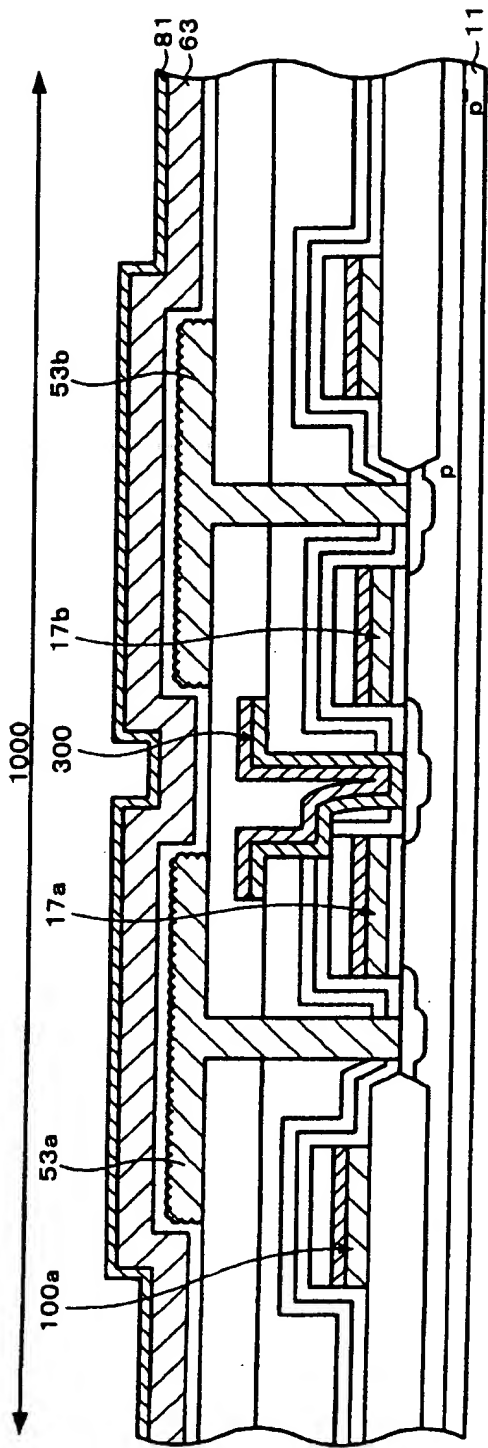


Fig. 20

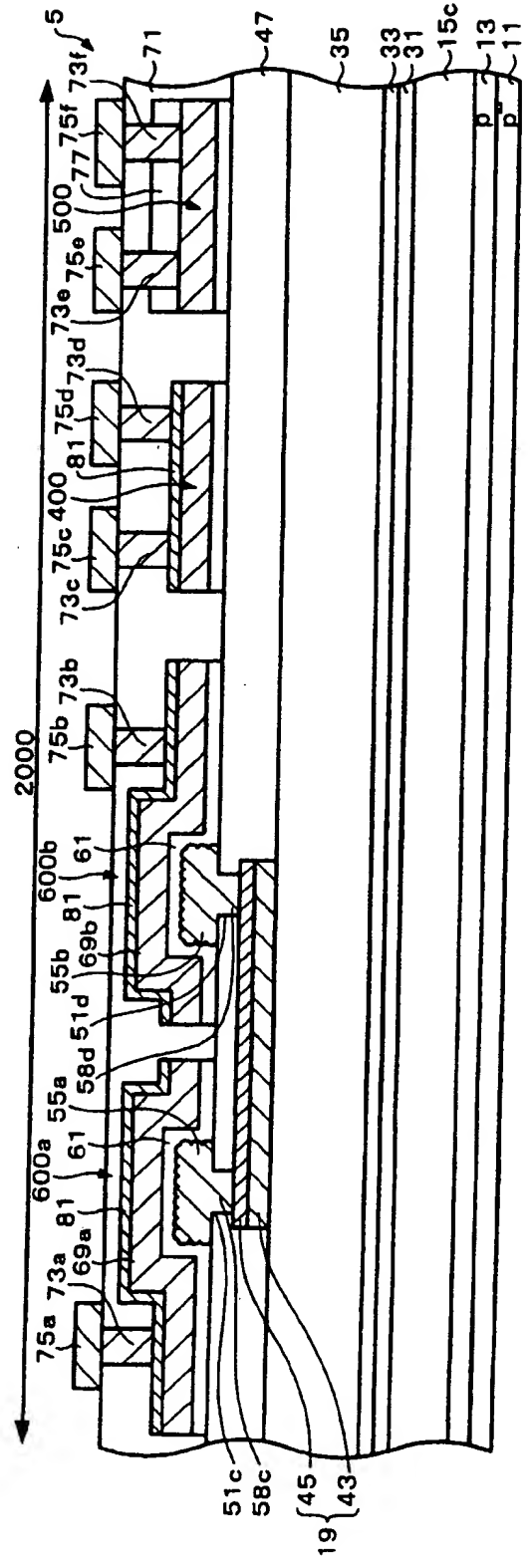
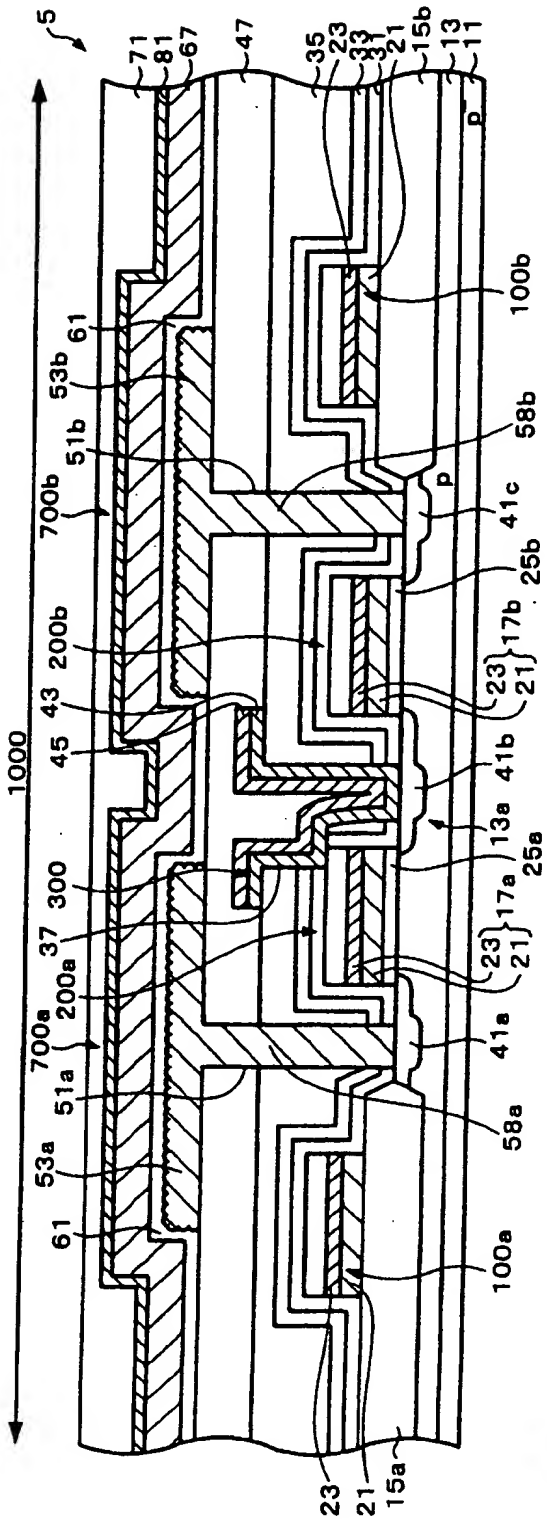
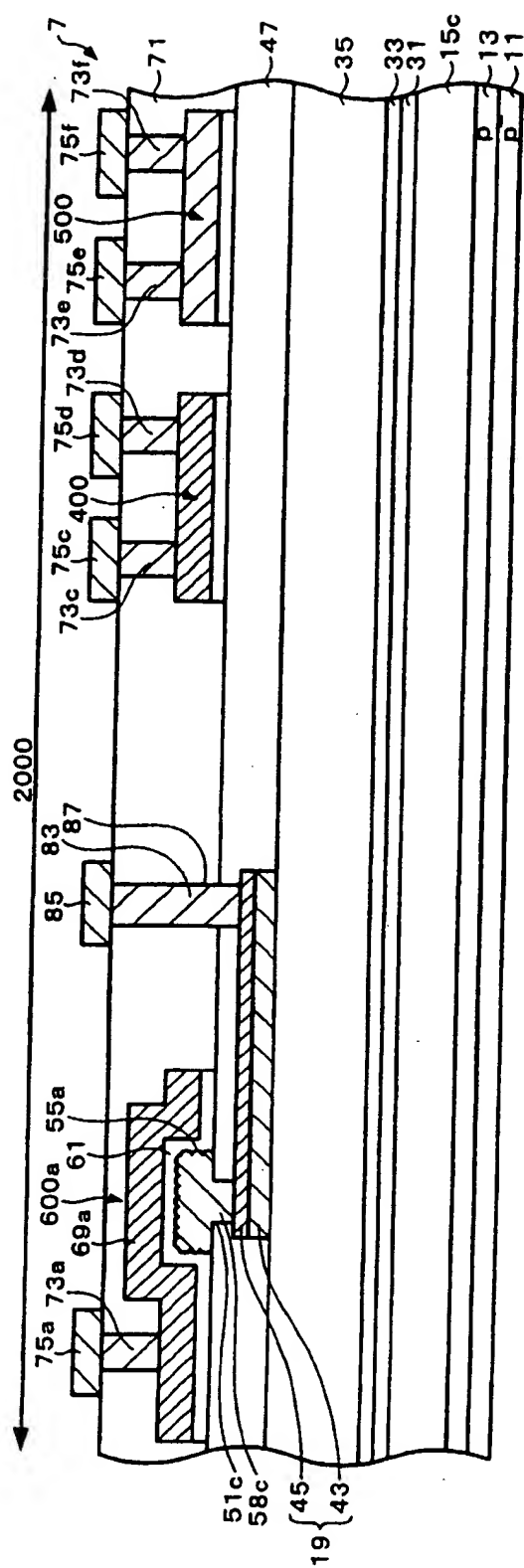
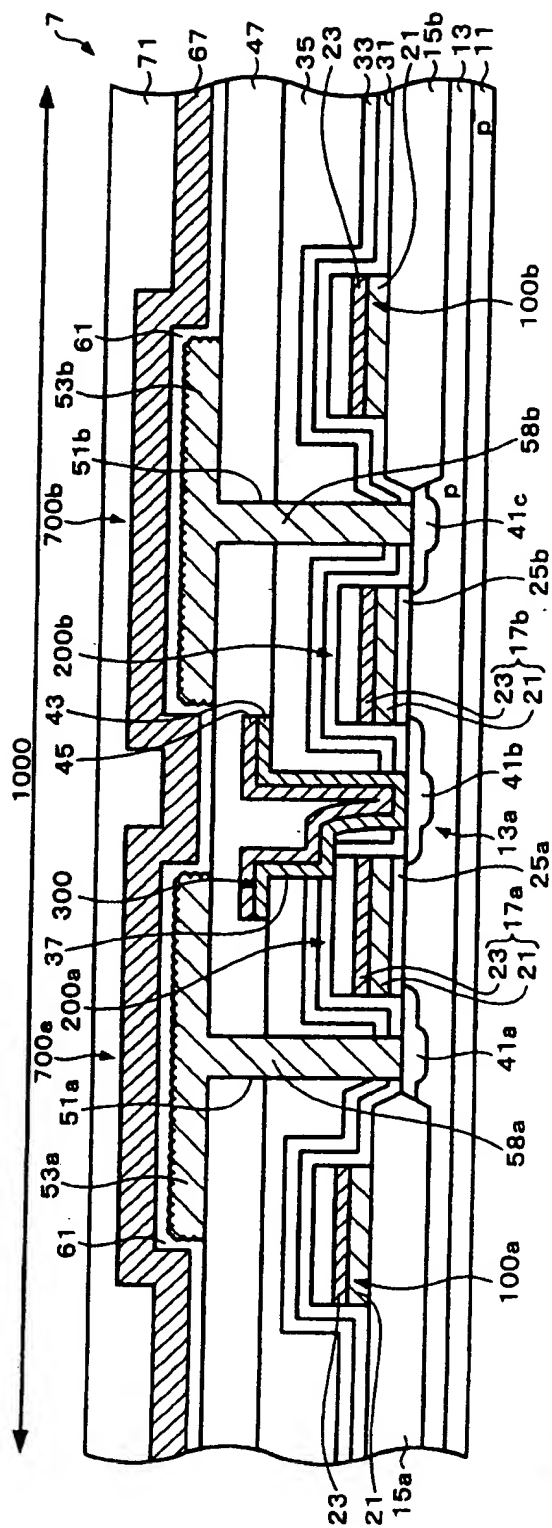


Fig. 21



**Fig. 22**